



22/105
12/6/03
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Farnworth et al.

Serial No.: 09/589,841

Filed: June 8, 2000

For: STEREO LITHOGRAPHIC
METHODS FOR FORMING A
PROTECTIVE LAYER ON A
SEMICONDUCTOR DEVICE SUBSTRATE
AND SUBSTRATES INCLUDING
PROTECTIVE LAYERS SO FORMED

Confirmation No.: 1415

Examiner: G. Lee

Group Art Unit: 2825

Attorney Docket No.: 2269-3923US
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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the document listed on attached Form PTO-1449 or PTO/SB/08 be considered by the Examiner and made of record. A copy of the listed document is enclosed pursuant to 37 C.F.R. § 1.98(a).

Serial No. 09/589,841

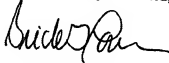
In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

<u>U.S. Patent No.</u>	<u>U.S. Patent Documents</u>	<u>Publication Date</u>	<u>Patentee</u>
US - 6,524,346 B1		02/25/2003	Farnworth

Applicants offer to supply any explanation or discussion of the document which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is filed concurrent with the filing of an RCE in the above-identified application, therefore no fee is due.

Respectfully submitted,



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Date: October 7, 2003

BGP/dlm:djp

Enclosures: Form PTO-1449 or PTO/SB/08
Copy of documents cited

Document in ProLaw